

10/650337

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,831,517 B1
DATED : December 14, 2004
INVENTOR(S) : Hedberg et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8,

Line 32, reads "I_b.", should read -- I_{b4}. --

Column 9,

Line 20, reads "output-current", should read -- output-stage bias current --

Line 22, reads "and low-current", should read -- and mid-current --

Column 10,


Line 27, reads "V_{bc2}" should read -- V_{bc2} --

Column 11,

Line 59, equation 16, reads "I_{b2}=I_{b1}", should read -- I_{c4}=I_{b2}+I_{b1} --

Signed and Sealed this

First Day of March, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office